



ARM1136/76 Software Development

Summary:

This course is designed for those who are writing software for systems based around the ARM1136J(F)-S, and ARM1176JZ(F)-S cores. Including an introduction to the ARM product range and supporting IP, the course covers the ARMv6-A cores and their feature sets.

Prerequisites:

- Some knowledge of embedded systems
- Knowledge of C and/or assembly language programming is useful
- A basic awareness of ARM is useful but not essential

Audience:

Software design engineers who need to write software for the ARM1136J(F)-S or ARM1176JZ(F)-S cores.

Length:

3 days

Modules:

- Introduction to the The ARM Architecture
- ARMv6-A Processor Cores
- Memory Sub-systems and Memory Management
- ARMv6 Instruction Sets
- Exception Handling
- ARM/Thumb Interworking
- ARMv6-A Interrupts
- Programming the GIC
- ARMv6-A Cache
- ARMv6-A Memory Management
- OS Support
- Memory Barriers
- Synchronisation
- ARM Tools Overview
- Embedded Software Development
- ARMv6-A Debug and Trace
- TrustZone