



## **ARM Bus Technologies**

### **Summary:**

This course is intended for engineers designing silicon devices using ARM's on-chip interconnect and bus fabric technology. The course covers the AHB, AXI and APB components of the AMBA standard, together with the NIC-301 network interconnect product. It also includes an overview of AMBA Designer and an overview of the new AMBA4 standard.

### **Prerequisites:**

- A working knowledge of system-on-chip design
- Familiarity with ARM technology is an advantage

### **Audience:**

Hardware design engineers who need to understand and work with the AMBA on-chip interconnect and related technologies.

### **Length:**

1 day

### **Modules:**

- AHB-Lite Protocol
- AXI Protocol
- AXI Interconnection Architectures
- APB
- NIC-301
- AMBA Designer
- AMBA 4 Overview