



ARM Cortex-M0+ Hardware Design

Summary:

This course is designed for those who are designing hardware based around the ARM Cortex-M0+ cores.

Prerequisites:

- Some knowledge of embedded systems
- Familiarity with digital logic and hardware/ASIC design issues
- A basic awareness of ARM is useful but not essential

Audience:

Hardware design engineers who need to understand the issues involved when designing SoC's around the ARM Cortex-M0+ core.

Length:

2 days

Modules:

- Introduction to ARM
- Cortex-M0+ Overview
- Programmers' Model
- Memory Model
- Exception Handling
- SysTick Timer
- AHB-Lite
- Cortex-M System Design Kit
- Cortex-M0+ Core
- Cortex-M0+ System Interfaces
- Cortex-M0+ Memory Protection
- Cortex-M0+ Integration Example
- Cortex-M0+ Power Management
- Cortex-M0+ Implementation & Integration
- Cortex-M0+ Debug
- Cortex-M0+ Trace