



## **Fast Model Fundamentals**

### **Summary:**

This course is designed for those who are using ARM's Fast Models both standalone and integrating into 3rd party tools. The course covers generation of Virtual Platforms, basic writing and debugging component models, using ARM Fast Models, importing C++ models and SystemC export, Trace, Debug and scheduling consideration.

### **Prerequisites:**

- Knowledge of embedded system design principles
- Familiarity with embedded software development
- Experience of the C++ programming language

### **Audience:**

Hardware/System Design Engineers who need to model system-on-chip designs and Software Engineers who need to develop and simulate applications.

### **Length:**

3 days

### **Modules:**

- Introduction to the Fast Models
- The Fast Models Portfolio: core, peripheral and system models
- Fast Models modelling technology
- Understanding LISA+
- PV Bus models
- Importing C++ models
- SystemC export
- Debugging with Fast Models: ARM, LISA+ and C++/System code
- Model Trace Interface
- Fast Models scheduler